

Notice of References CitedApplication No.
08/941,832

Applicant(s)

Chacon

Examiner

Hugh JonesGroup Art Unit
2763

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U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
	A	5,544,348	8/96	Umeda et al.	395	500.27
	B					
	C					
	D					
	E					
	F					
	G					
	H					
	I					
	J					
	K					
	L					
	M					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
	N						
	O						
	P						
	Q						
	R						
	S						
	T						

NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	U	Corbett et al.: "Modeling Just-in-Time Production Systems"; IEEE 1993 Winter Simulation Conf.; pp. 819-828	12/93
	V	Wiwakanond et al.: "Simulation of Electronics Manufacturing Systems with Two-Card Kanban"; IEEE 28th Proc. S.E. Symp. System Theory; pp. 391-395	4/96
	W	Seppanen; "Kanban Simulator using Siman and Lotus 1-2-3"; IEEE Winter Simulation Conf; pp. 838-844	12/93
	X		